17.05 hrs.

Title: Regarding passing of the Semiconductor Integrated Circuits Layout- Design Bill, 2000 by the Rajya Sabha at its sitting hel on the 15thMay, 2000.

SECRETARY-GENERAL: Sir, I have to report the following message received from the Secretary-General of Rajya Sabha:-

> "In accordance with the provisions of rule 111 of the Rules of Procedure and Conduct of Business in the Rajya Sabha, I am directed to enclose a copy of the Semiconductor Integrated Circuits Layout-Design Bill, 2000 which has been passed by the Rajya Sabha at its sitting held on the 15th May, 2000."

Sir, I lay on the Table the Semiconductor Integrated Circuits Layout-Design Bill, 2000 as passed by Rajya Sabha on the 15th May, 2000.